

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Steven J. Koester

**Examiner:** Anh D. Mai

**Serial No:** 10/717,279

**Art Unit:** 2814

**Filed:** November 19, 2003

**Docket:** YOR920030533US1(17110)

**For:** SEMICONDUCTOR FIELD-EFFECT  
TRANSISTOR HAVING STRAINED-LAYER  
(As Amended)

**Dated:** February 23, 2009

**Confirm. No:** 7401

Commissioner of Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**RESPONSE UNDER 37 C.F.R. § 1.111**

Sir:

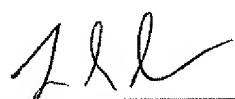
In response to the Office Action dated November 26, 2008, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

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**CERTIFICATE OF ELECTRONIC FILING**

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Dated: February 23, 2009

  
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Leslie S. Szivos, Ph.D.